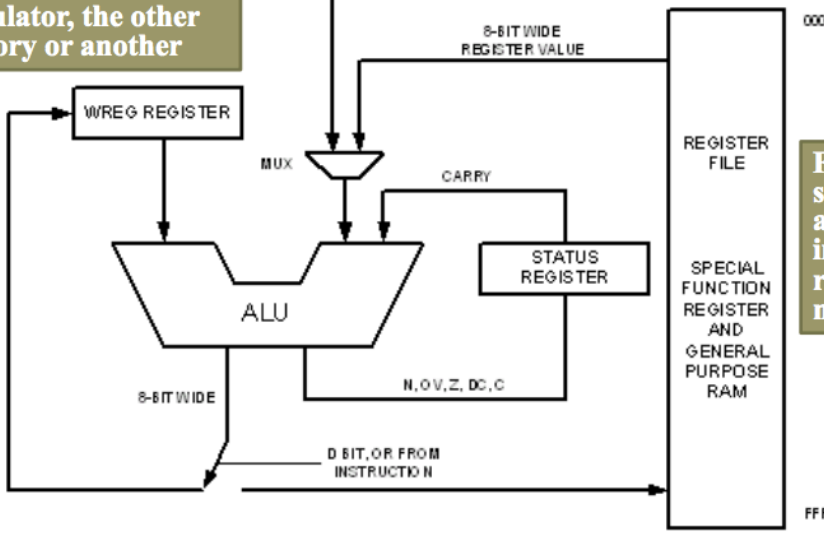


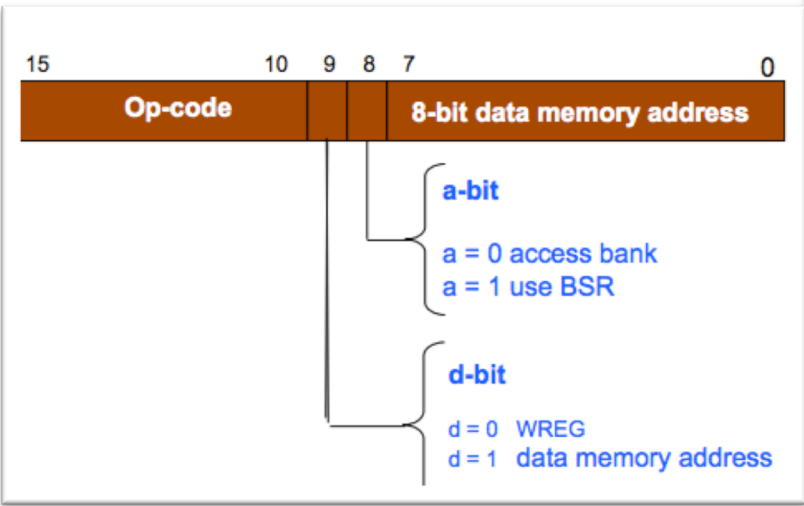
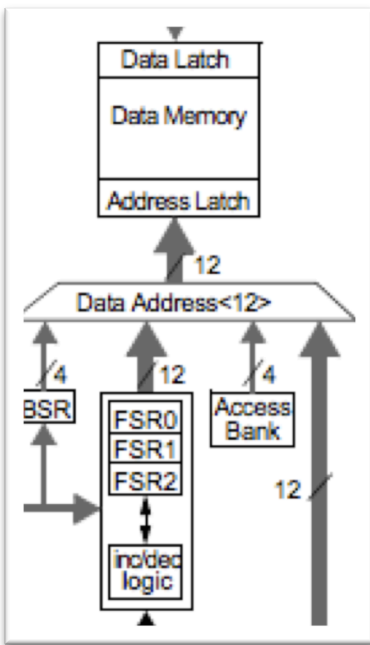
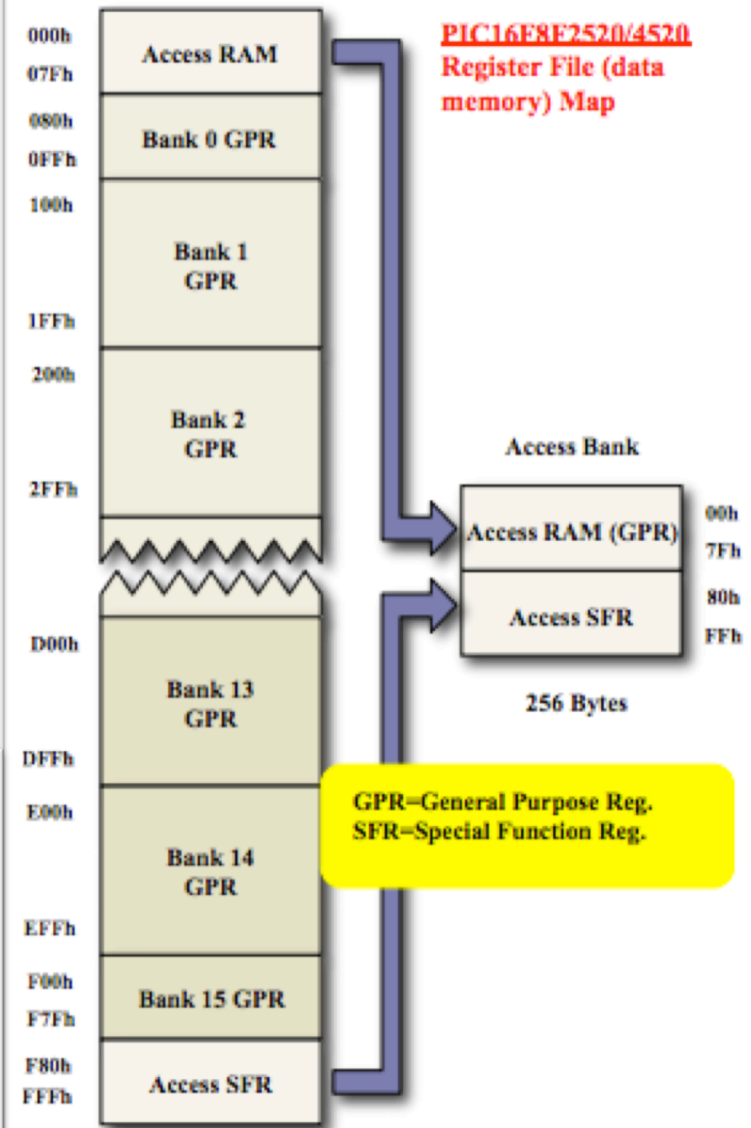
One number to be manipulated comes from the accumulator, the other from memory or another

8-BIT LITERAL (FROM INSTRUCTION WORD) An eight bit instruction informs the ALU which operation it is to carry out.



Flags in the status register are set to indicate the result, such as negative etc

PIC16F8E2/520/4520 Register File (data memory) Map

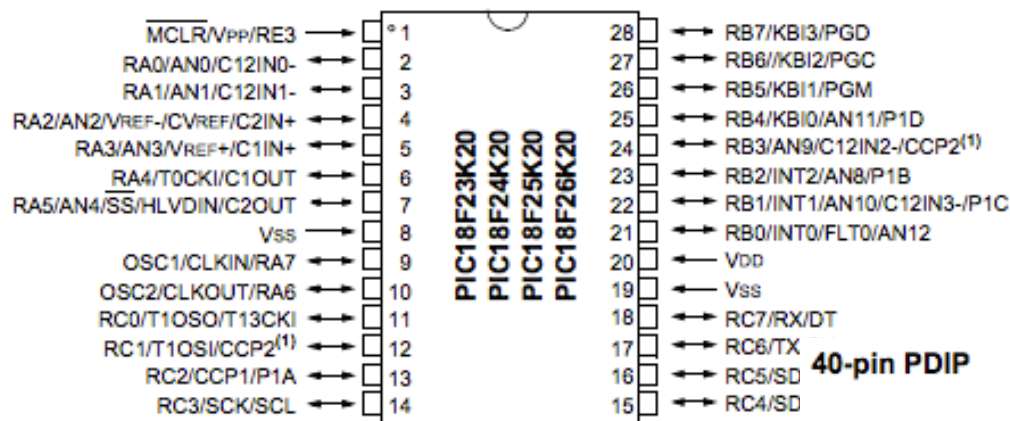


Device	Program Memory		Data Memory		I/O ⁽¹⁾	10-bit A/D (ch) ⁽²⁾	CCP/ ECCP (PWM)	MSSP		EUSART	Comp.	Timers 8/16-bit
	Flash (bytes)	# Single-Word Instructions	SRAM (bytes)	EEPROM (bytes)				SPI	Master I ² C™			
PIC18F23K20	8K	4096	512	256	25	11	1/1	Y	Y	1	2	1/3
PIC18F24K20	16K	8192	768	256	25	11	1/1	Y	Y	1	2	1/3
PIC18F25K20	32K	16384	1536	256	25	11	1/1	Y	Y	1	2	1/3
PIC18F26K20	64k	32768	3936	1024	25	11	1/1	Y	Y	1	2	1/3
PIC18F43K20	8K	4096	512	256	36	14	1/1	Y	Y	1	2	1/3
PIC18F44K20	16K	8192	768	256	36	14	1/1	Y	Y	1	2	1/3
PIC18F45K20	32K	16384	1536	256	36	14	1/1	Y	Y	1	2	1/3
PIC18F46K20	64k	32768	3936	1024	36	14	1/1	Y	Y	1	2	1/3

Note 1: One pin is input only.

Note 2: Channel count includes internal fixed voltage reference channel.

28-pin PDIP, SOIC, SSOP



40-pin PDIP

